DP 2.1 LL CTS

HDCP 2.3 CTS

16K UHD

UCD-500 Gen2

DisplayPort 2.1 Video Generator & Analyzer

UCD-500 Gen 2 is a video generator and analyzer unit with 16G capture memory. It supports testing DP Sinks and Sources with UHBR 20 Max Bit Rate Capability with both USB-C and DP connectors. 10, 13.5 and 20 Gbp/s lane rates and video modes up to 8K@60Hz and 16K@60 Hz (with DSC) are supported.

DP 2.1 features such as Adaptive-Sync, Panel Replay, eDP test functions, DSC, FEC and LTTPR are also supported.

Compliance Test Tool

UCD-500 Gen2 is a Compliance Test Tool for validating DP 2.1 & DP 1.4a Link Layer on DP Sinks and Sources. DisplayID/EDID and Adaptive-Syncs CTS tests are also supported.

Besides DP 2.1 Link Layer CTS tests, UCD-500 Gen2 also supports USB-4v1 DP tunneling CTS. UCD-500 Gen2 is also a Compliance Test Tool for HDCP 2.2/2.3 on DP Transmitters, Receivers and Repeaters.

For more infomation, visit: www.unigraf.fi

Highlights

- 16K Video Generator & Analyzer
- DP and USB-C inputs and outputs
- 10, 13.5 and 20 Gbp/s lane rates
- 8K@60Hz without compression
- 16K@60Hz with DSC
- DP 2.1 Link Layer CTS
- MST up to 4 streams
- DP 2.1 MST CTS for testing Source DUT
- Suitable for USB4 DP Tunneling CTS
- HDCP 2.3 CTS for receivers, transmitters and repeaters
- Link Analyzer (see rear page)
- Adaptive-Sync
- Panel Replay
- eDP test functions
- Support for DSC, FEC and LTTPR
- Color depth 6 to 16bpc
- Color formats: RGB, YCbCr 444, 422, 420
- Capture memory 16G

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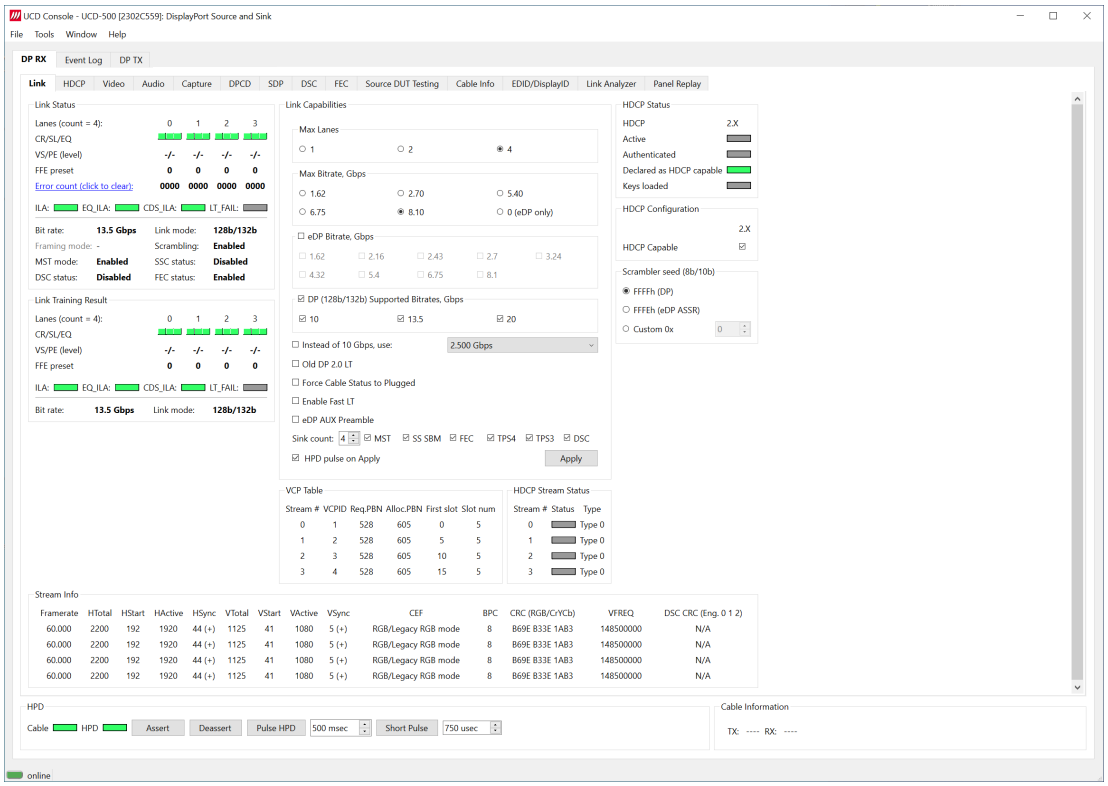
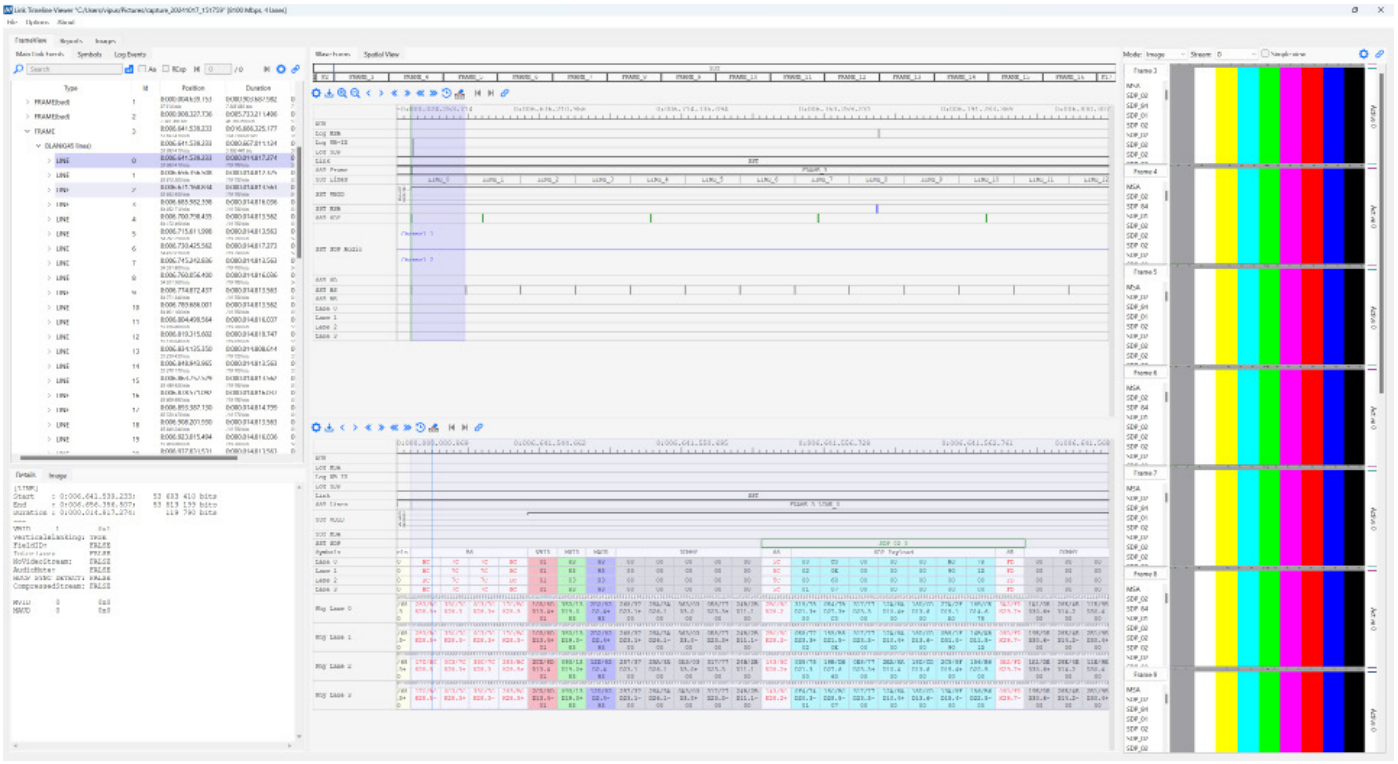
Link Status

Link Status displays the status of the link training and link parameters negotiated between UCD-500 Gen2 and the DUT. Status updates automatically.



DP Link Analyzer

DisplayPort Link Analyzer is a tool for capturing Main-link Data Events and AUX Transactions. For each bulk capture, the data is parsed in a timeline view presenting the captured frames. Within each frame, users can have a deep view of the events and metadata that occurs in each line. Measure distances in nanoseconds and symbol size to identify the length of problems in between events. Observe descrambled and scrambled symbols in hexadecimal format in each of the active lanes.



Link Timeline Viewer shown above, is a tool for evaluating the content and timing of data captured with Link Analyzer.

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